Serial No.: 10/595,424 **PATENT**

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is ____ being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Commissioner for Patents address below.

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/Mark D. Saralino/ Mark D. Saralino November 3, 2006

Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Moriyoshi NAKASHIMA et al.

Serial No.: 10/595,424

Filing Date: April 18, 2006

For: SEMICONDUCTOR CHIP MOUNTED INTERPOSER,

SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP INTERPOSER FABRICATION METHOD, BARE CHIP MOUNTED INTERPOSER, AND INTERPOSER SHEET

Examiner: Unknown

Art Unit: Unknown

Attorney Docket No. KOMOP0113US

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUBMISSION OF ENGLISH PRELIMINARY REPORT ON PATENTABILITY

Sir:

The English Translation of the Preliminary Report on Patentability for corresponding Application No. PCT/JP2003/016012 is now available. A copy of the Report is attached.

Serial No.: 10/595,424 **PATENT**

The copy of the English Translation of the Preliminary Report on Patentability is being submitted for the Examiner's benefit.

Respectfully submitted,

RENNER, OTTO, BOISSELLE & SKLAR, LLP

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DATE: November 3, 2006

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